## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Tetsuo K	AWA	NO, e	et al.			
Application No.:				Group Art Unit:		
Filed: Ma	d: March 24, 2004			Examiner:		
For: TIMING ANALYSIS APPARATUS, TIMING ANALYSIS METHOD AND PRODUCT						
			INFORMATION DISCLOS	SURE STATEMENT		
Commiss PO Box 1 Alexandr	1450					
Sir:						
provided the subje	certa ct U.	in info S. pat	ormation which the Examiner m	ovisions of 37 CFR § 1.56, there is hereby ay consider material to the examination of that the Examiner make this information of the subject application.		
1. Enclosure		Encl	osures accompanying this Infor	mation Disclosure Statement are:		
	1a. 1b. 1c. 1d.		application or a PCT Internatio English language translation (c each non-English language pu Explanations of Relevancy of F	complete or relevant portion(s)) attached to blication. References (ATTACHMENT 1(e), hereto) for		
, <sup>/</sup> 2.	$\boxtimes$		ccordance with 37 CFR § 1.98,	n of each non-English publication.  a concise explanation of what is presently each non-English language publication is		
	2a.		enclosed "English-language ve indicates the degree of relevan 609, Minimum Requirements for	s 2a, 2b, 2c and/or 2d) sh language publications were cited on the ersion of the search report or action which ace found by the foreign office". (See MPEP or an Information Disclosure Statement, Part Relevance, pp. 600-100 to 600-101, Rev. 1,		
	2b.	$\boxtimes$	set forth in the application.			

	satisfied because an Eng portion(s)) is attached to enclosed as Attachment	each non-English	slation (complete or relevant language publication.
to be, ma (other that	aterial to patentability nor a an search report(s) from a	a representation the counterpart foreign	is Statement is, or is considered nat a search has been made gn application or a PCT 37 CFR §§ 1.97(g) and (h).
	R	espectfully submit	tted,
	S	TAAS & HALSEY	LLP
Dated: March 24, 2 1201 New York Ave Washington, D.C. 2 Telephone: (202) 43 Facsimile: (202) 43	., N.W., Suite 700 20005 34-1500	By: H. J. Sta Registra	Haar Jas tion No. 22,010

FORM PTO-1449

# U.S. DEPARTMENT OF COMMERCE

ATTORNEY DOCKET NO. 1450.1040

APPLICATION NO.

PATENT AND TRADEMARK OFFICE

### LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

Tetsuo KAWANO, et al.

FILING DATE

March 24, 2004

FIRST NAMED INVENTOR

GROUP ART UNIT

#### **U.S. PATENT DOCUMENTS**

0.0.1.7.1.21.1.0							
EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA					_	
	АВ						
	AC						
	AD						
	AE		· · · · ·				
	AF						

#### **FOREIGN PATENT DOCUMENTS**

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSI YES	ATION NO
AG							
АН							
Al							
AJ							
AK							•
AL							

OTHER REFERENCES (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

		TRANSI YES	NO
AM	PATENT ABSTRACTS OF JAPAN of JP 63098042 A dated April 28, 1988.		
AN	PATENT ABSTRACTS OF JAPAN of JP 2002222232 A dated August 9, 2002.		
AO			

**EXAMINER** DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

#### ATTACHMENT 1(e)

# EXPLANATIONS OF RELEVANCY OF REFERENCES

1	ATTORNEY DOCKET NO.	APPLICATION NO.
	1450.1040	
	FIRST NAMED INVENTOR	
	Tetsuo KAWANO, et al.	
	FILING DATE	GROUP ART UNIT
	March 24, 2004	

Reference AM: A timing analysis in consideration of variations is carried out with use of delay value distribution based on normal distribution.

Reference AN: A logic simulation is executed considering effects of dispersion inside a chip per cell.